



Modified 02-03

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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application / Conf. No.	10/783,216 / 6678
	Filing Date	February 20, 2004
	First Named Inventor	Tim Tuan
	Examiner Name	Unknown
	Art Unit	2816
	Patent No.	
Mail Stop:	Attorney Docket Number	X-1462-1P US
Express Mail Receipt No.		
Total Number of Pages in This Submission		

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<input type="checkbox"/> Preliminary Amendment	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
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<input checked="" type="checkbox"/> Substitute PTO-1449(s) IDS by Applicant (PTO/SB/08A)	Remarks	
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Customer Number	24309 (Customer Number)	Reg. Number 51,959
Attn:	Justin Liu	
Signature		
Date	July 30, 2004	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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PATENT

IN THE UNITED STATES PATENT OFFICE

Applicant: Tim Tuan et al.
Assignee: Xilinx, Inc.
Title: "Tuning Programmable Logic Devices for Low-Power Design Implementation"

Serial No.: 10/783,216 File Date: 02/20/2004
Examiner: Unknown Art Unit: 2816
Docket No.: X-1462-1P US Conf. No.: 6678

COMMISSIONER FOR PATENTS
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Alexandria, Virginia 22313-1450

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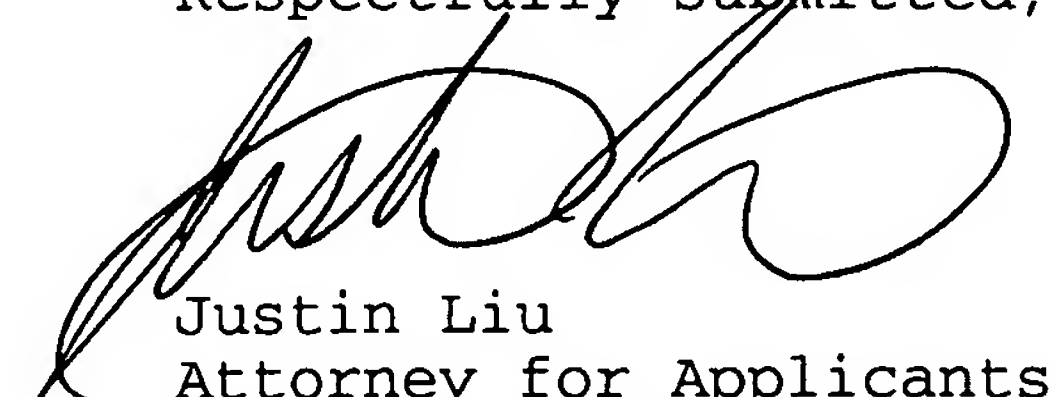
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the six (6) references listed in the attached Substitute for Form PTO-1449A/PT. One copy each of the four non-patent references is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

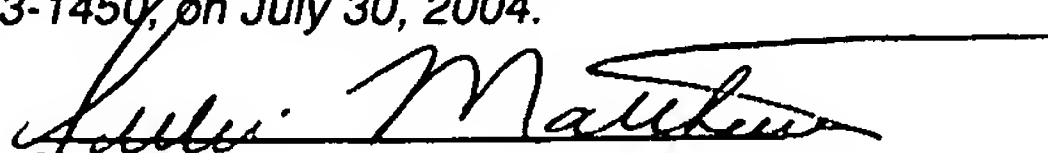
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Respectfully submitted,


Justin Liu
Attorney for Applicants
Reg. No. 51,959

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Julie Matthews
Name


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JL: jam



Substitute for form 1449A/PTO

(use as many sheets as necessary)

Sheet

1

of

2

Application / Conf. No.

10/783,216

6678

Filing Date

February 20, 2004

First Named Inventor

Tim Tuan

Art Unit

2816

Examiner Name

Unknown

Attorney Docket Number

X-1462-1P US

[illegible][illegible]Date
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application / Conf. No.	10/783,216 / 6678
				Filing Date	February 20, 2004
				First Named Inventor	Tim Tuan
				Art Unit	2816
				Examiner Name	Unknown
Sheet	2	of	2	Attorney Docket Number	X-1462-1P US

OTHER – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Jae Y. Park, Mark G. Allen; "A Comparison of Micromachined Inductors with Different Magnetic Core Materials"; 1996 Electronic Components and Technology Conference; 1996 IEEE; pp. 375-381.	
		Thomas D. Burd et al.; "A Dynamic Voltage Scaled Microprocessor System"; 2000 IEEE; IEEE Journal of Solid-State Circuits, Vol. 35, No. 11, November 2000; pp. 1571-1580.	
		Anthony J. Stratakis et al.; "A Low-Voltage CMOS DC-DC Converter for a Portable Battery-Operated System"; 1994 IEEE; pp. 619-626.	
		Anthony John Stratakis; "High-Efficiency Low-Voltage DC-DC Conversion for Portable Applications," Chapter 3; "DC-DC Converter Fundamentals"; pp. 42-78.	

Examiner Signature		Date Considered	
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